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UTILITY PATENT APPLICATION **TRANSMITTAL**

(only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No.	1875.4360003
First Inventor	Manolito M. Catalasan
Title	Method for Manufacturing a Memory Cell for Modification of Revision Identifier in an Integrated Circuit Chip
Express Mail Label No.	27

	APPLICATION See MPEP chapter 600 concerning u		AD.	DRI	ESS TO:	Mail Stop Paten Commissioner f P.O. Box 1450 Alexandria VA	or Patents	2
1. 🛚	(Submit an original, and a duplica	te for fee processing)	7. [8. N	ucled	opendix) itide and/or Ar applicable, all n	nino Acid Seq	e, large table or Con uence Submission	nputer Program
3. 🛚	Specification (preferred arrangement set forth b - Descriptive title of the Invention - Cross Reference to Related Appli - Statement Regarding Fed sponso - Reference to sequence listing, a t or a computer program listing ap - Background of the Invention - Brief Summary of the Invention - Brief Description of the Drawing	cations red R & D able, pendix	b		i. CD-l ii. pape Statements ver	nce Listing on: ROM or CD-R (r rifying identity of	2 copies); or	TS
_	Detailed Description Claim(s) Abstract of the Disclosure		9. 10.	⊠ □	Assignment Pa		et & document(s))	-N
 4.	Drawing(s) (35 U.S.C. 113 ath or Declaration	(Total Pages $\frac{27}{9}$)	11.		(when there is a			
	a. Newly executed (originb. Copy from a prior appli	cation (37 CFR 1.63(d))	12.		Information Di Statement (IDS		Copies of IDS Citations	
	i. DELETION	al with Box 18 completed) OF INVENTOR(S) ent attached deleting inventor(s)	13. 14.		Two Return R (Should be specified)	eceipt Postcards	s (MPEP 503)	
named in the prior applica 1.63(d)(2) and 1.33(b).		prior application, see 37 CFR 1.33(b).	15.					
6. Application Data Sheet. See 37 CFR 1.76.			16.			Request under 35 to or its equivalent.	U.S.C. 122 (b)(2)(B)(i). A	pplicant must attach
			17.	X X			37 C.F.R. § 1.136(a)(3 gnee is: Broadcom Co	-
	f a CONTINUING APPLICAT in Application Data Sheet under 37	TION, check appropriate box, and su CFR 1.76:	pply th	e requ	isite informatio	n below and in a	a preliminary amendmo	ent,
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consid	ered a part of the disclosure of the	APPS only: The entire disclosure of t accompanying continuation or divisionadvertently omitted from the submitted	nal app	licatio	n and is hereby	ich an oath or de incorporated by	eclaration is supplied ureference. The incorp	nder Box 5b, is oration <u>can only</u>
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This collection of information is required by 37 CFR 1.53(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you are required to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO:: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

FEE TRANSMITTAL for FY 2004

ffective 10/01/2003. Patent fees are subject to annual revision.

☐ Applicant claims small entity status. See 37 CFR 1.27

TOTAL AMOUNT OF PAYMENT (\$)1262.00

	Complete if Kn wn
Application Number	To b assign d
Filing Date	H r with n Oct ber 31, 2003
First Named Inventor	Manolito M. Catalasan
Examiner Name	To be assign d
Art Unit	To b assign d
Attorney Docket No.	1875,4360003

		Attorney	DOCKE	K NO.	1875.43600	103	
METHOD OF PAYMENT (check all that apply)					EE CALCULATION (c	continued)	
☐ Check ☑ Credit card ☐ Money Order ☒ Other** ☐ None	3. AD	DITION	AL FE	ES			
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☐ Charge any additional fee(s) during the pendency of this application ☐ Charge fee(s) indicated below, except for the filing fee to the above-identified deposit account.			1812	2,520	For filing a request for	ex parte reexamination	
			1804	920*	Requesting publication action	of SIR prior to Examiner	
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Total Claims 29 - 20** = 9 X 18 = 162.00	1460	130	1460	130	Petitions to the Commis	ssioner	
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Code (\$)	8021	40	8021	40	Recording each patent property (times number	assignment per r of properties)	40.0
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1201 86 2201 43 Independent claims in excess of 3 1203 290 2203 145 Multiple dependent claim, if not paid	1810	770	2810	385	For each additional inve (37 CFR 1.129(b))	ention to be examined	
1204 86 2204 43 **Reissue independent claims							
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SUBMITTED BY	Donici	tration *	<u></u>			Complete (if applicable)	
Name (Print/Type) Donald J. Featherstone	Registration No. (Attorney/Agent) 33				33,876	Telephone 202-371	-2600
Signature Sould Stuling Source						Date 10/31/	03

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This collection of information is required by 37 CFR 1.17 and 1.27. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



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October 31, 2003

WRITER'S DIRECT NUMBER: (202) 772-8629 INTERNET ADDRESS: DONF@SKGF.COM

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Mail Stop Patent Application

Re:

U.S. Non-Provisional Utility Patent Application under 37 C.F.R. § 1.53(b)

Appl. No.: To be assigned; Filed: Herewith on October 31, 2003

Method for Manufacturing a Memory Cell for Modification of Revision

Identifier in an Integrated Circuit Chip

Inventor(s):

Catalasan et al.

Our Ref:

1875.4360003

Sir:

The following documents are forwarded herewith for appropriate action by the U.S. Patent and Trademark Office:

- 1. PTO Fee Transmittal (Form PTO/SB/17);
- 2. Credit Card Payment Form PTO-2038;
- 3. PTO Utility Patent Application Transmittal (Form PTO/SB/05);
- 4. Authorization to Treat a Reply as Incorporating an Extension of Time under 37 C.F.R. § 1.136(a)(3);
- 5. Recordation Form Cover Sheet;
- 6. An original executed Assignment to Broadcom Corporation, recordation of which is hereby respectfully requested;

Sterne, Kessler, Goldstein & Fox P.L.C.: 1100 New York Avenue, NW: Washington, DC 20005: 202.371.2600 f 202.371.2540: www.skgf.com

7. U.S. Utility Patent Application entitled:

Method for Manufacturing a Memory Cell for Modification of Revision Identifier in an Integrated Circuit Chip

and naming as inventor(s): Manolito M. Catalasan

Vafa J. Rakshani Edmund H. Spittles

Tim Sippel Richard Unda

the application consisting of:

- a. An Application Data Sheet (37 C.F.R. § 1.76);
- b. A facsimile copy of an original Declaration, executed by Manolito M. Catalasan, Vafa J. Rakshani and Richard Unda;
- c. A facsimile copy of an original Declaration, executed by Edmund H. Spittles;
- d. A facsimile copy of an original Declaration, executed by Tim Sippel;
- e. A specification containing:
 - i. 20 pages of description prior to the claims;
 - ii. 5 pages of claims (29 claims, 2 independent);
 - iii. a one (1) page abstract;
- f. 27 sheets of drawings: (Figures 1, 2, 3A-3C, 4, 5A, 5B, 6, 7A-7D, 8A, 8B, 9A-9D, 10A, 10B, 11A-11D, 12A-12D, 13, 14, 15A-15D, 16, 17A-17C, 18A-18C, 19A-19C, 20A-20C, 21A-21C and 22); and
- 8. Two (2) return postcards.

It is respectfully requested that, of the two attached postcards, one be stamped with the filing date of these documents and returned to our courier, and the other, prepaid postcard, be stamped with the filing date and unofficial application number and returned as soon as possible.

Commissioner for Patents October 31, 2003 Page 3

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Donald J. Featherstone Attorney for Applicants

Registration No. 33,876

DJF/mmb SKGF_DC1:194627.1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re application of:

Catalasan et al.

Appl. No. To be assigned

Filed: Herewith on October 31, 2003

For:

Method for Manufacturing a

Memory Cell for Modification of

Revision Identifier in an Integrated Circuit Chip

Confirmation No. To be assigned

Art Unit: To be assigned

Examiner: To be assigned

Atty. Docket: 1875.4360003

Authorization to Treat a Reply as Incorporating an Extension of Time under 37 C.F.R. § 1.136(a)(3)

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

The U.S. Patent and Trademark Office is hereby authorized to treat any concurrent or future reply that requires a petition for an extension of time under this paragraph for its timely submission, as incorporating a petition for extension of time for the appropriate length of time. The U.S. Patent and Trademark Office is hereby authorized to charge all required extension of time fees to our Deposit Account No. 19-0036, if such fees are not otherwise provided for in such reply.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Donald J. Featherstone Attorney for Applicants

Registration No. 33,876

Date

:: <u>10/31/03</u>

1100 New York Avenue, N.W. Washington, D.C. 20005-3934 (202) 371-2600 DJF/mmb SKGF_DCI:194634.1